

IN THE SPECIFICATION

Please amend the title as follows:

**TRANSLATION LOOKASIDE BUFFER-BASED MEMORY SYSTEM AND
METHOD FOR ADDRESSING MEMORY AND TRANSFERRING DATA FOR USE IN A
COMPUTER HAVING A PLURALITY OF PROCESSOR ELEMENT**

Please amend the paragraph beginning at line 10, page 5, as follows:

In some embodiments, each processing element 110 is implemented as a vector processor such as described in co-pending application serial number [[XXXX]] 10/045,591 entitled “INSTRUCTIONS FOR TEST & SET WITH SELECTIVELY ENABLED CACHE INVALIDATE” filed on even date herewith, which is incorporated in its entirety by reference. Further details regarding various embodiments of processors 110 can be found in U.S. Patent Nos. 4,128,880, 5,349,677, 5,434,970, 5,526,487 and 5,623,685 each of which is incorporated by reference.